

Notice of References Cited	Application/Control No. 10/555,475		Applicant(s)/Patent Under Reexamination SUGITA ET AL.	
	Examiner Barry Drennan		Art Unit 4133	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,327,260 A	07-1994	Shimomae et al.	358/448
*	B	US-5,412,489 A	05-1995	Hirota, Yoshihiko	358/461
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Benedetti et al. (August 1998) "Image Convolution on FPGAs: the Implementation of a Multi-FPGA FIFO Structure," 24th Euromicro Conference, vol. 1 pp. 123-130.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.